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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: Amir Alon et al

Serial No.: 10/091,934

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Confirmation No.: 7058

Date: September 29, 2005

Group Art Unit: 2825

Examiner: Naum B Levin

Title: AN INTERCONNECT-AWARE METHODOLOGY FOR INTEGRATED CIRCUIT DESIGN

Honorable Commissioner for Patents

P.O. 1450

Alexandria, Virginia 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this paper (Amendment and Remarks, RCE and Petition for Extension of Time - total 6 pages) is being facsimile transmitted under Rule 37 CFR 1.6(d) to the U.S. Patent and Trademark Office to 571-273-8300 on September 29, 2005.

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Amendment

Sir:

This amendment is filed in response to the Office Action dated 03 June 2005 issued by the USPTO in connection with the above-identified patent application. A response to the 03 Sept 2005 Office Action was due 03 September 2005. Applicants are concurrently filing a Petition for a one-month extension of time, including the required fee. Therefore a response is due 03 October 2005. Accordingly, this Amendment is being timely filed.

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